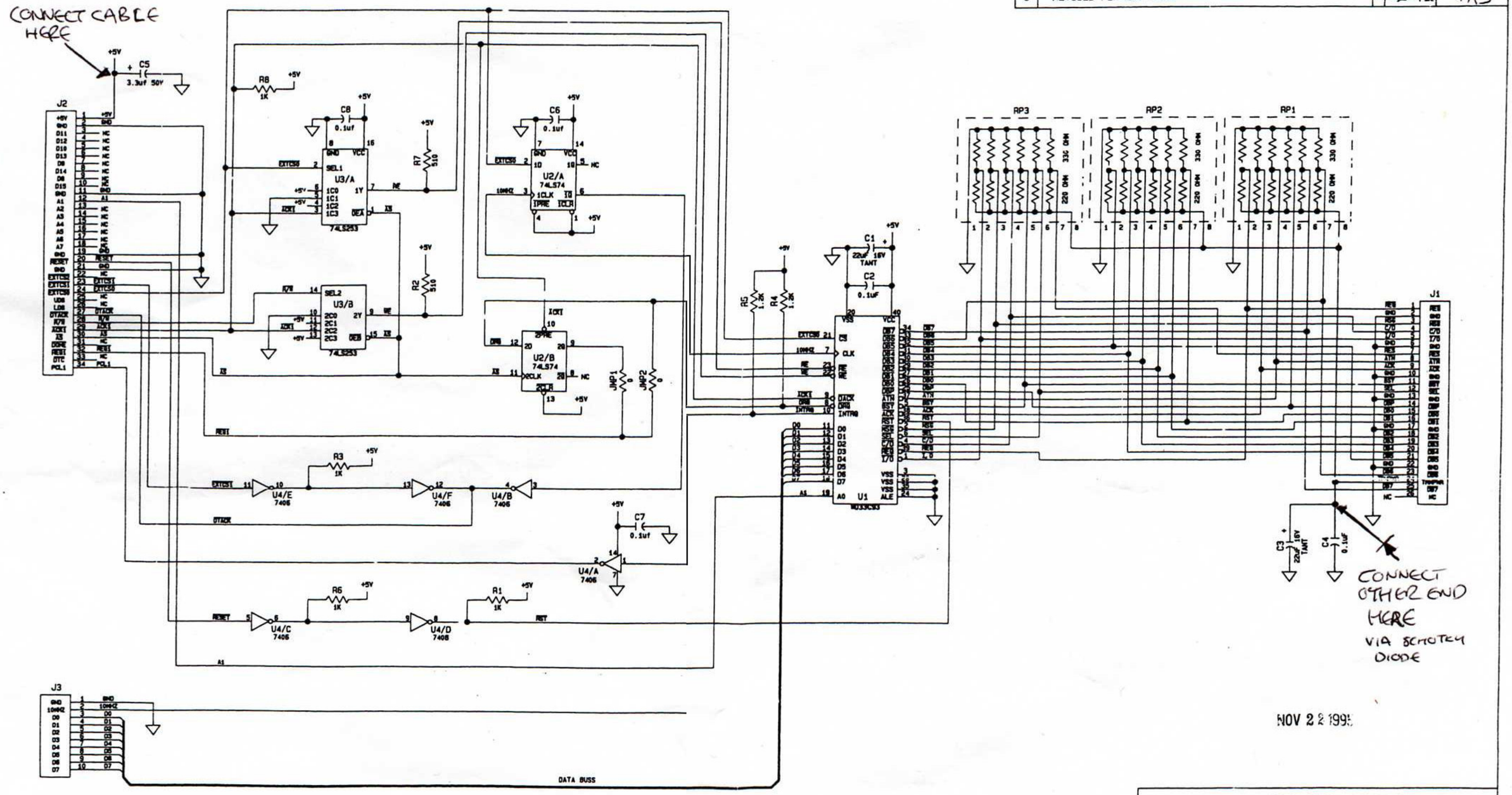
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
1	PROTOTYPE RELEASE	07-18-90	AKS
A	PRODUCTION RELEASE	8-28-90	AKS
B	REVISED PER ECO 2476	1-27-92	AKS
C	REVISED PER ECO 2528	4-2-92	AKS



NOV 2 2 199

3. ALL 0.1uf BYPASS CAPACITORS ARE 50V +80-20% Z5U.
2. ALL CAPACITOR VALUES ARE IN MICROFARADS, 16V MIN.
1. ALL RESISTOR VALUES ARE IN OHMS, 1/8W, 5%.

NOTES - UNLESS OTHERWISE SPECIFIED:

ENSONIQ CORP
155 GREAT VALLEY PKWY, MALVERN PA 19355

APPROVALS	DATE	PCB SCHEMATIC SCSI INTERFACE SP-2		
DRAWN BY: B.CARR	3-27-92			
CHECKED BY:		SIZE D	DWG. NO. 4010014701	REV C
APPROVED BY:				
USED ON: EPS-16+		SCALE —	SHEET 1 OF 1	